

IN THE SPECIFICATION:

Please amend the title of the invention to read as follows:

“Microprocessor including return prediction unit configured to determine whether a stored return address corresponds to more than one call instruction”

Please replace paragraph 60 on page 22 of the specification, beginning “As noted earlier, in some embodiments...” with the following paragraph.

[0060] As noted [[earlier]]earlier, in some embodiments, the entire return storage 252 may be invalidated when a mispredicted return instruction is detected. An additional situation where the entire storage may be invalidated is if the contents of instruction cache 204 are invalidated. For example, instruction cache 204 may be invalidated due to a task switch. Since the contents of return storage 252 may be invalid for the new task, the return storage’s entries may be invalidated. The contents of the return storage 252 may also be invalidated if the return prediction unit 250 incorrectly predicts a return address (as determined when the return instruction for which the prediction was made completes execution).